Appl. No. 10/766,073 Amdt. dated Feb. 28, 2007 Reply to Office action of Nov. 28, 2006

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-26. (canceled)

27. (currently amended) <u>A testing system, comprising:</u>

a multiplexer;

a plurality of oscillators coupled to the multiplexer;

a plurality of dividers coupled to the multiplexer;

wherein the testing system selects among the oscillators by configuring a selector on the multiplexer;

a PLL coupled to at least one of the plurality of dividers and synthesizing an oscillation frequency of the selected oscillator; and

a DUT that receives the synthesized frequency from the PLL and also receives a divided frequency from at least one of the plurality of dividers;

The testing system of claim 26, wherein the synthesized frequency received by the DUT f_1 and the oscillation frequency of the selected oscillator f_2 are synchronized according to the equation $n \cdot f_1 = m \cdot f_2$, where n and m are integer numbers.

28. (original) The testing system of claim 27, wherein the oscillation frequency of the selected oscillator is used as a timing reference for the testing system.

Appl. No. 10/766,073 Amdt. dated Feb. 28, 2007 Reply to Office action of Nov. 28, 2006

29. (currently amended) The testing system of claim [[24]] <u>27</u>, wherein the oscillator is a crystal oscillator.